

AMENDMENTS TO THE CLAIMS

Claims 1-28 (Canceled).

29. (Previously Presented) A liquid crystal display device, comprising:
a substrate;
a closed main seal made of a UV hardening sealant on the substrate;
a closed dummy seal made of a UV hardening sealant in a region between the main seal and an edge of the substrate; and
a UV shielding part formed at a location where the dummy seal intersects with a cell-cutting line.

30. (Previously Presented) The device of claim 29, wherein the UV shielding part is formed under the dummy seal.

31. (Previously Presented) The device of claim 29, wherein the UV shielding part is formed over the dummy seal.

32. (Previously Presented) The device of claim 29, wherein the UV shielding part is formed under and over the dummy seal.

33. (Previously Presented) The device of claim 29, wherein the UV shielding part is formed between the dummy seal and the substrate.

34. (Previously Presented) The device of claim 29, wherein the UV shielding part is formed at all intersections of the dummy seal and the cell-cutting line.

35. (Previously Presented) The device of claim 29, wherein the UV shielding part is formed at intersections of the dummy seal and the cell-cutting line at at least one side edge of the substrate.

36. (Previously Presented) The device of claim 29, wherein the UV shielding part is formed at intersections of the dummy seal and the cell-cutting line at opposite side edges of the substrate.

37. (Previously Presented) The device of claim 29, wherein the substrate includes a gate line, a data line, a thin film transistor and a pixel electrode.

38. (Previously Presented) The device of claim 37, wherein the UV shielding part is formed on the same layer as the gate line.

39. (Previously Presented) The device of claim 37, wherein the UV shielding part is formed on the same layer as the data line.

40. (Previously Presented) The device of claim 29, wherein the substrate includes a gate line, a data line, a thin film transistor, a pixel electrode and a common electrode.

41. (Previously Presented) The device of claim 29, wherein the substrate includes a light-shielding layer, a color filter layer and a common electrode.

42. (Previously Presented) The device of claim 41, wherein the UV shielding part is formed on the same layer as the light-shielding layer.

43. (Previously Presented) The device of claim 29, wherein the substrate includes a light-shielding layer and a color filter layer.

44. (Previously Presented) The device of claim 29, further comprising a column spacer on the substrate.

45. (Currently Amended) A method for manufacturing an LCD device, comprising: forming a UV shielding part in a dummy region on one of a first substrate and a second substrate;

forming a closed main seal made of a UV hardening sealant on the substrate on which the UV shielding part is formed;

forming a closed dummy seal made of a UV hardening sealant on the substrate between the main seal and an edge of the substrate;

applying a liquid crystal on one of the first and second substrates;

attaching the first and second substrates; and

irradiating UV light onto the main seal and dummy seal;

wherein the UV shielding part is formed at a location where the dummy seal intersects with a cell-cutting line.

46. (Previously Presented) The method of claim 45, further comprising cutting the attached substrates into unit cells.

47. (Previously Presented) The method of claim 45, further comprising:
forming gate and data lines on the first substrate;
forming a thin film transistor at a crossing point of the gate and data lines; and
forming a pixel electrode on the first substrate.

48. (Previously Presented) The method of claim 47, wherein the UV shielding part is formed with the gate line.

49. (Previously Presented) The method of claim 47, wherein the UV shielding part is formed with the data line.

50. (Previously Presented) The method of claim 45, further comprising:
forming a light-shielding layer on the second substrate; and
forming a color filter layer on the light-shielding layer.

51. (Previously Presented) The method of claim 50, wherein the UV shielding part is formed with the light-shielding layer.

52. (Previously Presented) The method of claim 45, wherein the UV light is irradiated to the surface of the substrate on which the UV shielding part is formed.

53. (Previously Presented) The method of claim 45, further comprising heating the attached substrates after irradiating UV light to the attached substrates.

54. (Previously Presented) The method of claim 46, wherein cutting the substrates includes scribing and breaking processes in one step.

55. (Previously Presented) The method of claim 45, wherein irradiating UV light includes masking an active area inside the main seal.

56. (Previously Presented) The method of claim 45, wherein the main and dummy seals are formed on the second substrate, and the liquid crystal is applied on the first substrate.

57. (Previously Presented) The method of claim 45, wherein the main and dummy seals are formed on the first substrate, and the liquid crystal is applied on the second substrate.

58. (Previously Presented) The method of claim 45, further comprising forming a column spacer on one of the first and second substrates.

Claim 59 (Canceled).